



Comparison of Pixel to Pixel Controllers

	MCTRL 300	MCTRL 700	MCTRL 660	MCTRL 660Pro	MCTRL R5	MCTRL 4K
INPUTS						
Input Interface	DVI x 1 Audio	DVI x 1 (loop) HDMI1.3 x 1 (loop) Audio	DVI x 1 (loop) HDMI1.3 x 1 (loop) Audio	DVI x 1 (loop) HDMI1.3 x 1 (loop) 3G SDI x 1 (loop)	HDMI1.4 x 1 DL DVI x 1 6G SDI x 1	DP1.2 x 1 HDMI2.0 x 1 DL DVI x 2
Input Bit Depth	8 Bit	8 Bit	8/10/12 Bit	8/10/12 Bit	8/10/12 Bit	8/10/12 Bit
Max Input Width	3840	3840	3840	3840(DVI, HDMI) 1920(SDI)	3840	7680
Max Input Height	3840	3840	3840	3840(DVI, HDMI) 1080(SDI)	3840(DVI, HDMI) 1080(SDI)	3840
OUTPUTS						
Output Ports	2 x RJ45	6 x RJ45	4 x RJ45	6 x EtherCon	8 x EtherCon	16 x EtherCon
Fiber Outputs	x	x	x	2 x 10G OPT (1 Primary, 1 Backup)	2 x 10G OPT (1 Primary, 1 Backup)	4 x 10G OPT (2 Primary, 2 Backup)
CAPACITY						
Loading Capacity	1.3 million pixels	2.3 million pixels	2.3 million pixels	2.3 million pixels	4.14 million pixels	8.8 million pixels
Max Output Width	3840	3840	3840	3840(DVI, HDMI) 1920(SDI)	3840	7680



Max Output Height	3840	3840	3840	3840(DVI, HDMI) 1080(SDI)	3840(DVI, HDMI) 1080(SDI)	3840
GENLOCK						
Genlock	×	×	×	Tri-level, Bi-level / Black burst	Tri-level, Bi-level / Black burst	Tri-level, Bi-level / Black burst
CONTROL & PROTOCOL						
Control	COM (USB)	COM (USB)	COM (USB)	1G Ethernet / USB	1G Ethernet / USB	1G Ethernet / USB
Protocol	RS232	RS232	RS232	TCP/IP / USB 2.0	TCP/IP / USB 2.0	TCP/IP / USB 2.0
KEY FEATURES						
LCD Module	×	×	√	√	√	√
Decimal Frame Rate	×	×	×	23.98 / 29.97 / 47.95 / 59.94 / 71.93 / 119.88 Hz	×	23.98 / 29.97 / 47.95 / 59.94 / 71.93 / 119.88 Hz
Low Latency mode	×	×	×	<1ms (60Hz) ^{①②③}	×	<1ms (60Hz) ^{①②}
3D	×	×	×	×	×	√
HDR	×	×	×	×	×	√
Free Rotation	×	×	×	×	A8s/A10s Pro	×
144Hz Input	×	×	×	×	×	√

Note:

1. The maximum width of data wiring per port must not exceed 512 pixels if low latency is enabled.
2. Low latency is not available when using Genlock.
3. Low latency is not available when using SDI.